

## **ABSTRACT OF THE DISCLOSURE**

A data transmission apparatus 1 transmits data signals "S1" and "S2" containing identical serial data "ds" in two frequency bands, and a data reception apparatus 2 receives those data signals "S1" and "S2." Parallel data "dp1" and "dp2" obtained from the data signals "S1" and "S2" is subjected to selection or synthesis in a subcarrier selection/demodulation circuit 26 so as to be demodulated into serial data "dsx," which is then outputted to a MAC section 50.